

ABSTRACT OF THE DISCLOSURE

An MPEG data recording apparatus stands by while inputting MPEG data output from an IEEE 1394 interface (1) to an FIFO buffer section (14) and starts to record the MPEG data in the FIFO buffer section (14) at the time of a recording start operation of a user, and records all the MPEG data which have already been input into the FIFO buffer section (14) at the time of a recording stop operation of the user and then stops the recording operation. Obtained is the MPEG data recording apparatus capable of quickly responding to the recording start operation and the recording stop operation of the user when recording the MPEG data input through the IEEE 1394 interface by the recording operation of the user.